KEVIN YING

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EDUCATION

Cornell University, College of Engineering, Ithaca, NY — Expected May 2020
Bachelor of Science, Electrical and Computer Engineering — GPA: 4.12/4.3
Inducted IEEE-Eta Kappa Nu (2018) (Kappa), Dyson Business Minor for Engineers

Relevant Courses: Digital Systems Design Using Microcontrollers, Computer Architecture, Embedded Systems, Introduction to Analysis of Algorithms, Object Oriented Programming and Data Structures, Probability and Inference, Signals and Information, Discrete Structures (TA)

ENGINEERING EXPERIENCE

Alarm.com, Software Engineering Co-op, McLean, VA — Summer 2018, Spring 2019
- Implemented a new feature for home light automation, code pushed to internal beta.
- Constructed rules in proprietary assembly to interact with third party integrations.
- Developed and maintained web pages and backend services for .NET applications.
- Wrote and refactored automated unit tests for lights and thermostats, participated in code reviews.

Cornell University Autonomous Underwater Vehicle Project Team, Ithaca, NY — Oct. 2016-Present
- Designed, populated, and programmed printed circuit boards which deliver and monitor power.
- Collaborated with mechanical engineers to improve heat dissipation around critical components.
- Selected more reliable components to minimize power draw.
- Onboarded and mentored new members in one-on-one setting on soldering and PCB design.
- Secured 1st place out of 44 teams at 2017 AUVSI RoboSub competition, reached finals in 2018.

Johns Hopkins University Applied Physics Laboratory, Software Intern, Laurel, MD — Summer 2017
- Trained and evaluated convolutional neural networks in TensorFlow to perform multiple tasks in tandem, such as single-image super-resolution and deblurring.
- Implemented digital image processing functions in MATLAB and Python.

National Institute of Standards and Technology, Research Intern, Gaithersburg, MD — Summer 2015
- Developed LabVIEW program to automate two-hour per week scanning tunneling microscope task.
- Created graphical UI overlay and created safety measures allowing for overnight use.

ON-CAMPUS INVOLVEMENT

Teaching Assistant, Digital Logic and Computer Organization — Jan. 2018–Present
- Organize and hold lab sessions giving students hands-on experience with logic gates, Verilog, FPGAs
- Hold office hours, grade student work, assist professor in refining new course content.

Institute of Electrical and Electronics Engineers, Co-President — Jun. 2018-Present
- Lead student chapter of twenty-five undergraduate electrical and computer engineers
- Organize and host recruitment events and technical talks around campus for corporate sponsors
- Communicate with other on-campus organizations to coordinate joint events

SKILLS

Languages and Tools: C#, Java, MATLAB, Python, Assembly, Verilog, Confluence + Jira, Git

Electrical Skills: KiCAD, C microcontroller programming, I^2C, soldering, oscilloscope